

IN THE CLAIMS:

This listing of claims will replace all prior versions and listings of claims in the application:

Listing of Claims:

1-24 (Previously Cancelled).

25-31 (Currently Cancelled)

32 (Previously Presented). A circuit for detecting boundaries in a stream of digital samples, the circuit comprising:

- a memory for storing at least a portion of the stream of digital samples wherein each digital sample comprises a plurality of values;

- a plurality of comparators coupled to the memory, a first input of each comparator coupled to a first memory location storing a first digital sample and a second input of each comparator coupled to different memory locations wherein the different memory locations correspond to digital samples received prior to the first digital sample and at least one value of the first digital sample stored in the first memory location is compared with a corresponding value of each one of the digital samples stored in the different memory locations, each comparator is configured to output a one value if the comparison is equal and a zero value if the comparison is not equal; and

- a summing circuit coupled to the plurality of comparators, the summing circuit containing circuitry to add the outputs from the plurality of comparators and produce a correlation value.

33 (Previously Presented). The circuit of claim 32, wherein the circuit is configured to generate a correlation value after the receipt of a specified number of digital samples.

34 (Currently Amended). The circuit of claim 32, wherein ~~the~~each comparators is configured to output a one value if the digital samples being compared are within a specified difference of each other and the comparator will output a zero value if the digital samples being compared are outside of a specified difference of each other.

35 (Previously Presented). A communication device comprising:

- a transceiver to transmit and receive information being sent to and from the device;

- a transmit path coupled to the transceiver, the transmit path containing circuitry to convert the information from the device into a form suitable for transmission;

- a receive path coupled to the transceiver, the receive path containing circuitry to convert the information sent to the device into a form suitable for use; and

- a processor coupled to the transmit and receive paths, the processor containing circuitry to detect boundaries in a stream of digital samples, the processor comprising:

- a memory for storing at least a portion of the stream of digital sample values;

- a plurality of comparators coupled to the memory, a first input of each comparator coupled to a first memory location storing a first digital sample and a second input of each comparator coupled to different memory locations wherein the different memory locations correspond to digital samples received prior to the first digital sample and at least one value of the first digital sample stored in the first memory location is compared with a corresponding value of each one of the digital

samples stored in the different memory locations, each comparator is configured to output a one value if the comparison is equal and a zero value if the comparison is not equal; and

a summing circuit coupled to the plurality of comparators, the summing circuit containing circuitry to add the outputs from the plurality of comparators and produce a correlation value.